Serial No.: 10/849,350

AMENDMENTS TO THE CLAIMS

The following listing of claims replaces all prior listings of claims in the present

application.

What Is Claimed Is:

1. (original) A storage device that is detachably attachable to an information processing

apparatus, comprising:

an IC chip;

a first control unit extracting a control command for the IC chip included in a control

command for the storage device from the information processing apparatus; and

a second control unit performing interface conversion corresponding to the IC chip on

the control command for the IC chip extracted by the first control unit and giving the converted

control command to the IC chip.

2. (currently amended) The [[A]] storage device according to claim 1, wherein the second

control unit performs interface conversion on data sent from the IC chip and stores the converted

data in a predetermined storage area; and

the first control unit reads the data stored in the storage area in accordance with a

control command for the storage device from the information processing apparatus and gives the

read data to the information processing apparatus.

3. (currently amended) The [[A]] storage device according to claim 1, wherein the first control

unit receives a writing command for the storage apparatus device, in whose data area a control

2

Serial No.: 10/849,350

command for the IC chip is mapped, and extracts the control command for the IC chip mapped in the data area.

4. (currently amended) The [[A]] storage device according to claim 3, wherein the first control unit refers to an address area of the writing command for the storage device and, when an address is set therein which shows that the control command for the IC chip is mapped in the data area, extracts the control command for the IC chip from the data area.

5. (currently amended) The [[A]] storage device according to ~~any one of claims~~ claim 1, wherein the IC chip comprises a nonvolatile memory and has a security function.

6. (currently amended) The [[A]] storage device according to claim 2, wherein the first control unit receives a writing command for the storage ~~apparatus~~ device, in whose data area a control command for the IC chip is mapped, and extracts the control command for the IC chip mapped in the data area.

3